

ABSTRACT

A method and apparatus for high speed addressing of memory locations within the same page. Generally, a first selected location in the memory space is accessed using at least a first and second part of the address. Then, a first part of the address of a second selected location in the memory space is transmitted. The method and apparatus determine whether at least a second part of the address of the second selected location corresponding to the second part of the address of the first selected location is the same as the second part of the address of the first selected location, and determines the address for the second selected location without transmitting the second part of the address of the second selected location by joining the second part of the address of the first selected location with the first part of the address of the second location on the condition that the second part of the address of the second selected location is the same as the second part of the address of the first selected location.